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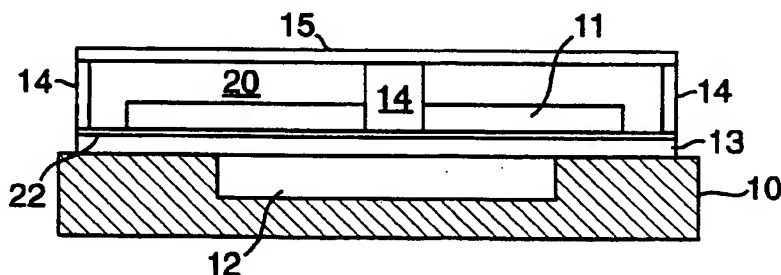
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(54) **Direct view display based on a micromechanical modulator**

(57) A direct-view display comprising an array of micro-mechanical modulators is disclosed. The modulator used to form the display comprises a suspended, vertically moving membrane (15) and a substrate (10). The device functions based on optical interference effects between the membrane (15) and the substrate (10) which cause the modulator to either substantially reflect or absorb an optical signal. The interference effects are a function of the size of the air gap (20) between the membrane and substrate, which varies as the membrane moves. The membrane moves in response to a data signal, representative of an image, delivered to the modulator. The display generates an image based on the pattern of light and dark sections of the display corresponding to the reflectivity of each modulator at a given point in time.

**FIG. 5c**



## Description

### FIELD OF THE INVENTION

This invention relates generally to direct view displays, and more particularly to direct view displays that use micromechanical modulators as pixel elements.

### BACKGROUND OF THE INVENTION

Recent advances in optomechanical technology, such as the development of micromodulators, have been applied to projection display systems.

A projection display comprised of micromodulators has been described by Younse in IEEE Spectrum at 27-31 (Nov. 93). In Younse's display, micromirrors are suspended over address electrodes and memory cells of a CMOS static ram. Each micromirror is associated with a particular memory cell. The micromirrors are suspended at diagonally opposed corners, which allows them to tilt under the action of bias. Bias is provided by a data signal which is stored on the address electrode. Electrostatic forces between the address electrodes and the mirror tilt the associated mirror either +10 degrees or -10 degrees. Light reflected from a mirror having a +10 degree tilt is imaged by a projection lens onto a screen. Light reflected from a mirror having a -10 degree tilt will not be imaged by the projection lens. Shading from white to black is determined based on the time during each video frame that the mirror is imaging through the projection lens. An image is created on the screen by the action of many such micromirrors.

The modulator described by Younse is only suitable for projection displays, since it works by deflecting light through specific angles. It may not be used for direct-view displays. For certain applications, such as very small displays, a projection-based display is unsuitable.

### SUMMARY OF THE INVENTION

A direct view display comprising micromechanical modulators as the pixel elements is disclosed. The micromechanical modulator used for the display is, preferably, a modification of the modulator described in our co-pending and commonly assigned application S.N. 08/187,676 filed January 27, 1994. The modulator comprises a substrate or plateau layer, and a membrane having at least one layer, spaced to form an air gap. The modulator further comprises electrodes so that bias may be applied to the modulator generating an electrostatic force that changes the position of the membrane relative to the substrate or plateau layer.

A plurality of such modulators are organized to form a display. Each of the modulators comprising the display is individually addressable through suitable drive electronics. In one embodiment, an address transistor is associated with each modulator. A data signal, representing an image, is carried along line address circuitry and

delivered to the transistor of a modulator. The transistor acts as a switch to sample and store the signal as a voltage on the modulator address electrode. Depending upon the address electrode potential, the membrane either maintains, or moves from, its rest position. The refractive indices of the membrane and substrate are chosen so that the change in position of the membrane results in a change in the reflectivity of the modulator. Light reflected onto the modulators is either substantially reflected or absorbed as a function of the membrane position. An image is created based on the pattern of light and dark sections of the display corresponding to the reflectivity of each modulator at a given point in time. A grey scale is developed based upon the amount of time, per video frame, a modulator is reflecting or absorbing.

### BRIEF DESCRIPTION OF THE DRAWINGS

Further features of the invention will become more apparent from the following detailed description of specific embodiments thereof when read in conjunction with the accompanying drawings, in which like elements have like reference numerals and in which:

FIG. 1 is an embodiment of a display according to the present invention;

FIG. 2 is a prior art method for controlling the display of FIG. 1;

FIG. 3 is an embodiment of drive electronics suitable for controlling the modulators of the present invention;

FIG. 4 is an embodiment of a micromechanical modulator;

FIGS. 5a-5c show several views of a first preferred embodiment of a micromechanical modulator;

FIGS. 6a-6c show several views of a second preferred embodiment of a micromechanical modulator;

FIGS. 7a-7c show several views of a third preferred embodiment of a micromechanical modulator; and

FIG. 8 shows the spectra for a display according to the present invention.

### DETAILED DESCRIPTION OF THE INVENTION

An embodiment of the direct-view display of the present invention, shown in Fig. 1, comprises a plurality of micromechanical modulators 3. Each modulator 3 may be considered to be a pixel of the display 1. A row-column pixel addressing scheme, referred to herein as the display drive electronics 2, allows each pixel of the

display to be individually addressed. Each pixel or modulator 3 has its own circuit elements, referred to herein as the modulator drive electronics 4. Since the modulator 3 is preferably fabricated entirely in silicon, the modulator drive electronics 4 may be co-fabricated with the modulator 3 so that the modulator and modulator drive electronics may be monolithically integrated. The display also includes a light source 5.

The pixel addressing arrangement described by L.J. Hornbeck in "Deformable-Mirror Spatial Light Modulators", Proc. SPIE, Vol. 1150 (Aug. 6, 1989) at 86-102 and J.M. Younse in IEEE Spectrum at 27-31 (Nov. 1993) is suitable for use in forming a display 1 according to the invention. These articles, as well as all other articles mentioned herein, are incorporated by reference.

Fig. 2 shows the display drive electronics 2 used to implement the addressing scheme of Younse. In one embodiment, the display drive electronics 2 may have a line-address format wherein the information to the modulators 3 is updated one line at a time. Exemplary modulator drive electronics 4 are shown in Fig. 3. Each modulator 3 has at least one address transistor 45. The address transistor 45 comprises a drain 36, a gate 40 and a source 44. Within one row of pixels, i.e., modulators 3, the gate 40 of an address transistor 45 is connected to the gate 40 of each adjacent address transistor 45 so that all gates 40 within the row are connected together. A gate decoder 50 drives each row of pixels. Within one column of pixels, each transistor drain 36 is interconnected and driven by a serial-to-parallel (S/P) converter 60 and by a drain driver amplifier 65.

Referring to FIGS. 2 and 3, data, corresponding to an image, is fed continuously into CCD shift registers 58. Once data has been loaded into the CCD shift registers, it is parallel-shifted to the drain drive amplifiers 65. After the data has been shifted, the gate decoder 50 activates one row of address transistors by sending a signal along gate line 32 which biases the gate 40 of each address transistor 45 in the row. The address transistor 45 can then deliver the data signal from the data line 30 onto the modulator address electrode 22. This address sequence is repeated one line at a time. In this manner, data representative of an image is transferred to each modulator 3.

Implementation of the display drive electronics 2 and the modulator drive electronics 4 is well known to those skilled in the art. The exemplary embodiments of the drive electronics 2, 4 that are shown in Figs. 2 and 3 are not intended to be limiting. Any suitable arrangement of drive electronics 2, 4 that will occur to those skilled in the art may be used in conjunction with the micromechanical modulator 3 described herein to form a display 1.

As will be discussed below in detail, the modulator 3 comprises a substrate or plateau layer and a membrane made of at least one layer, spaced from the plateau to form an air gap. The modulator further comprises two electrodes, one of which is the modulator address electrode 22. An electrostatic force is generated, or not,

as a function of the data signal delivered to the address electrode 22. If an electrostatic force is generated, the membrane moves relative to the substrate or plateau. The refractive indices of the membrane and substrate/plateau are chosen so that the change in position of the membrane results in a change in the reflectivity of the modulator 3. The varying reflectivity of each modulator 3 forming the display 1 is thus controlled by the data signal stored on the address electrode 22. Variation in pixel brightness throughout the display 1, corresponding to the status of each modulator at a given time, i.e., substantially reflecting or absorbing light, generates an image.

The structure, fabrication and operation of the micromechanical modulators 3 are fully described in S.N. 187,676, the specification of which is herein incorporated by reference in its entirety.

As described in S.N. 187,676, the micromechanical modulators utilize an optical interference effect to modulate an optical signal. The varying reflectivity of the modulator is primarily a function of the air gap 20 and the relationship of the refractive index of the optical membrane 15 and the refractive index of the layer, typically the substrate 10, which collectively define the air gap 20. When the refractive index of the optical membrane 15 is about equal to the square root of the refractive index of the substrate, an anti-reflection condition can be created. For an optical membrane 15 having a layer or layers  $\lambda/4$  in thickness, as measured within the membrane, and an air gap 20 of  $m\lambda/4$ , as measured within the ambient medium, the membrane 15 and air gap 20 function as a high reflection mirror where  $m$  is an odd integer and as an anti-reflection coating where  $m$  is 0 or an even integer.  $\lambda$  refers to the wavelength of the light incident upon the optical membrane 15. Thus, as the optical membrane 15 moves one-quarter of a wavelength of the incident light, a minimum or maximum reflectivity state will be encountered, depending upon the size of the air gap when the membrane is in the unbiased position. The ambient medium for the modulators can be, without limitation, air, partial vacuum, or a nitrogen or argon atmosphere.

While a plurality of micromechanical modulators 3, as described in S.N. 187,676, may be used to form a direct-view display 1, they are preferably modified as described herein: Fig. 4 shows the structure of an embodiment of the micromechanical modulator 3 as taught in S.N. 187,676. An optical membrane 15 is supported at each corner by a support arm 25. The positioning of the support arms dictates how closely adjacent modulators 3 may be placed to one another in forming the display 1. It should be appreciated that this support arm arrangement results in a low filling factor for the display.

Figs. 5a - 5c, 6a - 6c and 7a - 7c show several embodiments of a modification of the micromechanical modulator 3 of S.N. 187,676, wherein the supports for the optical membrane 15 are located more directly beneath the optical membrane. Since these supports do not protrude substantially beyond the edges of the opti-

cal membrane 15, modulators 3 having such a structure can be packed more closely than those having the structure shown in Fig. 4. Forming a display 1 using any of the exemplary embodiments of the modulators 3 as shown in Figs. 5 - 7 increases the filling factor, hence the resolution, of the display.

Figs. 5a - 5c show a first preferred embodiment of a micromechanical modulator 3 suitable for forming the display 1 of the present invention. The modulator 3 is shown in perspective in Fig. 5a and in a plan view in Fig. 5b. Fig. 5c is a cross section through the line AA in Fig. 5b. The optical membrane 15 is not shown in Fig. 5a, and shown cutaway in Fig. 5b to better illustrate the features of the modulator 3.

Referring to Figs. 5a - 5c, the modulator comprises an optical membrane 15 which is supported by pedestals 14 which rest on pedestal supports 13. A modulator address electrode 22 is located on top of the pedestal supports 13. Trenches 12 are located in the substrate 10 underneath the pedestal supports 13. A plateau 11 is located within the region defined by the pedestal supports 13. The pedestals 14 support the optical membrane 15 above the plateau 11 so that an air gap 20 is formed between the optical membrane and the plateau. The air gap 20 should be at least one-quarter of the median wavelength of the light signal being modulated. As described in detail in S.N. 08/187,676, the optical membrane 15 for this and all other embodiments of a modulator 3 suitable for use in the present invention can be comprised of one or two layers. In a two layer membrane, the additional layer functions as a strained layer for producing tension in the optical membrane 15. A second electrode (not shown in the Figures) should be located in the trench 12 if the substrate is not suitably conductive. Typically, the substrate 10 or the second electrode of each of the modulators comprising the display will be maintained at ground potential.

When a voltage difference exists between the modulator address electrode 22 and the substrate 10, an electrostatic force will be generated which causes the portion of the pedestal support 13 located over the trenches 12 to move into the trench. Since the optical membrane 15 is supported by pedestals 14 which rest on the pedestal supports 13, the optical membrane will move from its unbiased state toward the plateau 11 in response to the pedestal support's movement. As discussed above, the refractive indices of the optical membrane 15 and the plateau 11 are chosen so that the change in the position of the optical membrane results in a change in the reflectivity of the modulator 3.

The modulator 3 shown in Figs 5a- 5c may be formed as follows. A film of poly-silicon is first deposited on a standard p-doped silicon substrate, which may be in the form of a wafer. After deposition, the film is patterned, using standard photolithographic methods, into the plateau 11 for each display pixel. The film is deposited to a thickness equal to the sum of the height of pedestal 14, the thickness of pedestal support 13, and the

thickness of the modulator address electrode 22, minus at least one-quarter of the median wavelength of the light signal being modulated. Assuming a median wavelength of about 540 nanometers (nm), at least 135 nm should be subtracted from the aforementioned sum. This will place the top of the plateau 11 at least 135nm below the top of the pedestals 14. This arrangement will provide at least a one-quarter-wavelength air gap 20 between the unbiased optical membrane 15 and the plateau 11.

Alternately, the plateau 11 could be formed by appropriately masking the substrate 10 and then etching. Since electronics will be formed in the substrate, and forming electronics in etched silicon often results in defects, this method is not preferred. A third method for forming the plateau 11 comprises depositing silicon nitride on the substrate 10 and patterning the silicon nitride so that it covers the substrate at the locations where plateaus are desired. The substrate is then placed in an oxidation furnace, and conditions are controlled so that oxygen penetrates the silicon substrate, forming silicon dioxide, to a depth equal to the desired height of the plateau 11. The region underneath the silicon nitride will not be oxidized. Silicon dioxide and silicon nitride are removed, leaving plateaus at the desired location.

Next, an array of n-channel MOSFETs, one for each pixel of the display, is formed using standard IC fabrication. Trenches 12 are etched into the substrate to a depth approximately equal to the air gap 20 using appropriate etching methods, including, without limitation, reactive ion etching (RIE), ion milling, or wet chemical etching. These trenches are then filled with an appropriate sacrificial material, such as spin on glass (SOG), BPTEOS, high etch rate silicon nitride, aluminum, or copper. A film of mechanical material is then deposited on the surface over the trenches 12 and patterned into a ridge around each plateau 11. This ridge will form the pedestal support 13. The mechanical film can consist of any appropriate material, such as polysilicon, silicon nitride, polyimide or composites thereof.

Next, an electrically conductive material is deposited on the ridge and patterned to form the modulator address electrode 22. The conductive material can be doped polysilicon, or evaporated metals such as gold, or aluminum. Preferably, the modulator address electrode 22 should be formed from material, such as doped polysilicon, that can be anti-reflection coated. A second sacrificial layer of the same type as used to fill the trenches 12 is deposited over the wafer. This layer should be deposited to the desired thickness of the air gap 20. Holes are then formed in the second layer using an appropriate etching method, such as those previously discussed. These holes are then filled to the same height as that of the second layer with structural material, such as polysilicon, polyimide, silicon nitride or composites thereof. The structural material is then patterned into pedestals 14.

Next, a film of structural material, such as polysilicon, nitride, or polyimide is deposited and patterned into

the optical membrane 15. Finally, a suitable etch is used to remove all sacrificial material resulting in a suspended optical membrane 15 with integral pedestals, as shown in Fig. 5a.

A second preferred embodiment of a modulator 3 is shown in Figs. 6a - 6c. Fig. 6c is a cross section through line BB in Fig. 6b. The optical membrane 15 is not shown in Fig. 6a and shown in phantom in Fig. 6b to better illustrate the support beam arrangement.

As shown in Figs. 6a - 6c, four support beams 25 protrude from a beam support 24. A membrane support 26 is located at the terminus of each support beam 25. An optical membrane 15 is supported by the membrane supports 26.

The beam support 24 rests on top of a substrate 10. The beam support 24 is thicker than the support beams 25; that is,  $T_{bs} > T_{sb}$ . Since the support beams 25 protrude from the beam support 24 near the beam support's upper surface 27, an air gap 20 is defined between the optical membrane 15 and the substrate 10. A modulator address electrode 22 is located on the upper surface 27 of the beam support 24 and on the upper surfaces 28 and 29 of the support beams 25 and the membrane supports 26, respectively. The modulator address electrode 22 must be in electrical contact with modulator drive electronics 4 so that a data signal can be delivered to the modulator address electrode. As described for the previous embodiment, the drive electronics may be fabricated in the substrate contiguous to each modulator 3. The substrate should be appropriately doped so that it is conductive, or an electrode should be deposited on the substrate underneath the support beams 25. Additionally, an electrode can optionally be deposited at the perimeter of the optical membrane 15.

Under the action of bias, which is provided by a data signal, an electrostatic force is generated which causes the support beams 25 to bend toward the substrate. The deflection in the support beams 25 is greatest near the membrane support 26. The optical membrane 15 will be drawn towards the substrate 10 in conjunction with the deflection of the support beams 25. As previously noted and described in detail in our copending application S.N. 08/187,676, the change in position of the optical membrane 15 will result in a change of the reflectivity of the modulator 3.

A third preferred embodiment of a modulator 3 useful in conjunction with the present invention is illustrated in Figs. 7a - 7c. Fig. 7c is a cross section through line CC in Fig. 7b. Again, the optical membrane 15 is shown in phantom in Figs. 7a and 7b to better illustrate the support beam arrangement.

In a manner similar to the second preferred embodiment, four support beams 25 protrude from a beam support 24. A bridging beam 23 connects the support beams 25 to the beam support 24. The bridging beam 23 is located near the center of each side of beam support 24 and near its upper surface 27. Membrane supports 26 are located at each end of each support beam 25. An

optical membrane 15 is supported by the membrane supports 26.

The beam support 24 rests on top of a substrate 10. The beam support 24 is thicker than the support beams 25; that is,  $T_{bs} > T_{sb}$ . Since the support beams 25 are located near the beam support's upper surface 27, an air gap 20 is defined between the optical membrane 15 and the substrate 10. A modulator address electrode 22 is located on the upper surface 27 of the beam support 24 and on the upper surfaces 27 and 28 of the support beams 25 and the membrane supports 26, respectively. The modulator address electrode 22 must be in electrical contact with modulator drive electronics 4 so that a data signal can be delivered to the modulator address electrode. As described for the first preferred embodiment, the drive electronics may be fabricated in the substrate contiguous to each modulator 3. The substrate should be appropriately doped so that it is conductive, or an electrode should be deposited on the substrate underneath the support beams 25 and membrane supports 26.

Under the action of bias, which is provided by a data signal, the support beams 25 bend toward the substrate 10. The deflection in the support beams 25 is greatest near the membrane supports 26, which may contact the substrate. The center of the support beam 25 where the bridging support 23 joins the support beam will remain in a relatively fixed position relative to the substrate. The optical membrane 15 will be drawn towards the substrate 10 in conjunction with the deflection of the support beams 25. As previously noted, the change in position of the optical membrane 15 will result in a change of the reflectivity of the modulator 3.

The exemplary second and third preferred embodiments may be formed using the techniques described in our copending application S.N. 08/187,676. Differences in the shape, size and position of the various elements comprising the modulators described herein, as compared to the modulators of S.N. 08/187,676, will require using different masks to pattern such elements. The required modifications will be within the abilities of those skilled in the art.

In a most preferred embodiment of the modulator 3, the optical membrane 15 and the substrate 10 or plateau 11 are spaced so that the air gap 20, in the unbiased state, is one-quarter of a wavelength of the optical signal. Further, under the action of bias, the optical membrane 15 contacts the plateau so that the air gap 20 is zero. In the unbiased state, the optical membrane 15 and air gap 20 function as a high reflectivity mirror. In the biased state, the optical membrane functions as an anti-reflection coating for the plateau 11.

It is recognized that modifications of the present invention will occur to those skilled in the art in view of the present teaching. For example, it is not required that the pedestal support 13 of the first preferred embodiment, and the beam support 24 of the second and third preferred embodiments, be a closed mesa or ridge in the

form of a rectangle as shown in the drawings. For example, four individual ridges can be used to form such supports. It should also be appreciated, according to the teachings of S.N. 08/187,676, that the air gap 20 defined by the optical membrane 15 and the substrate 10 or plateau 11 may be greater than one-quarter of a wavelength of the optical signal. These and other modifications are considered to be within the contemplated scope of the present invention.

Micromechanical modulators according to the teachings of S.N. 08/187,676 have been made and tested. A large area modulator having a membrane of 100x100 microns with  $m=4$  (an air gap equal to one wavelength of the incident optical signal) using a 12 volt drive had a rise and fall time for the optical membrane of about 10 microseconds. Smaller devices have been fabricated with rise times of 250 nanoseconds.

A display according to the present invention has been modeled. The performance of a pixel of the display, i.e., the modulator, is shown in Fig. 8 as a plot of reflectivity of a modulator versus the wavelength of the optical signal at a number of viewing half-angles. The spectra collectively identified by the reference numeral 70 represents the high reflection condition for the modulator. The spectra collectively identified by the reference numeral 80 represents the low reflection condition of the modulator. The viewing half-angles, identified by reference numerals 72-78 at the high reflection condition and numerals 82-88 at the low reflection condition, refer to the position of an observer relative to the normal to the display, i.e., 0°, 15°, 30°, 45° and 60°. Referring to Fig. 8, greater than 7:1 contrast is obtained over the entire visible light spectrum for viewing half-angles as large as 45°. Approximately 70 percent reflectivity is obtained over the entire visible spectrum.

The optical signal is assumed to be unpolarized. If the light was polarized, the spectra would shift somewhat. The modulator modeled in Fig. 8 is assumed to have a two-layer membrane consisting of a nitride layer of 630 angstroms and a polysilicon layer of 300 angstroms. The refractive index of the nitride layer is 2.

The angle of incidence of the optical signal upon the modulators forming the display affects the spectra of the display. For the modulator modeled in Fig. 8, the light source was assumed to be ambient light, so that the viewing half-angles represent the angle of incidence of the optical signal upon the modulator. The air gap 20 should be adjusted where the angle of incidence is other than normal to the display, i.e., 0°. For an angle of incidence  $\Theta$ , the air gap should be  $m\lambda/4/(\cosine \Theta)$ . Thus, in the design of a display according to the invention, where ambient light is used as the light source, it is preferable to design the air gap based on an angle  $\Theta$  between about 0° and 45°. For the modulator modeled in Fig. 8, the air gap was based on an angle  $\Theta$  of 30°, though no particular angle is presently preferred. Thus, the modulator modeled in Fig. 8 has an unbiased air gap equal to  $1 \times (\lambda/4) / (\cosine 30^\circ)$ . In the low reflection con-

dition, the air gap is 0.

Further, since the refractive index of silicon varies as a function of the incident optical signal, an average value for the refractive index should be selected for the substrate 10, assuming it is silicon.

## Claims

1. A direct-view display comprising:
  - a plurality of micromechanical modulators which have a variable and controllable reflectivity to an incident optical signal, where the reflectivity is controlled via an optical interference effect;
  - drive electronics for individually controlling the reflectivity of each of the micromechanical modulators forming the plurality of micromechanical modulators by selective application of a data signal; and
  - a light source for generating the optical signal.
2. The direct-view display of claim 1 wherein the micromechanical modulators are organized in an array characterized by rows and columns, and each micromechanical modulator comprises:
  - a substrate having a refractive index;
  - an optically transparent membrane comprising at least a first layer of material, the first layer having a refractive index which is approximately equal to the square root of the refractive index of the substrate, and
  - a flexible support for positioning the membrane in a first position having a spaced and superposed relation to the substrate to form an air gap therewith, and the flexible support being deformable in response to a data signal to move the membrane to a second position, changing the air gap by approximately one-quarter of a wavelength of the optical signal, as measured in the ambient medium, where the change from the first to the second position causes a change in the amount of the optical signal that is reflected from the membrane.
3. The direct-view display of claim 1 where the modulators forming the display are characterized by a contrast ratio of at least about 7:1 over the entire visible light spectrum for viewing half-angles up to about 45 degrees.
4. The direct-view display of claim 1 where the modulators forming the display are characterized by a contrast ratio of at least about 7:1 over the entire visible light spectrum for viewing half-angles of at least about 40 degrees.
5. The direct-view display of claim 1 wherein the display is suitable for viewing at half-angles up to about 45 degrees.

6. The direct-view display of claim 1 wherein the display is suitable for viewing at half-angles of at least about 40 degrees.
  7. The direct-view display of claim 2 wherein the substrate is p-doped silicon. 5
  8. The direct-view display of claim 2 wherein the optical membrane is selected from the group consisting of polysilicon, silicon nitride and polyimide. 10
  9. The direct-view display of claim 2 wherein the flexible support is located substantially beneath the optical membrane. 15
  10. The direct-view display of claim 9 wherein the flexible support comprises a plurality of pedestals positioned over a plurality of trenches.
  11. The direct-view display of claim 2 wherein the flexible support comprises a plurality of flexible beams fixed to a support. 20
  12. The direct-view display of claim 2 wherein the air gap is an integer multiple of one-quarter of a wavelength of the optical signal when the membrane is in the first position. 25
  13. The direct-view display of claim 2 wherein each modulator further comprises an address electrode for receiving a data signal from the drive electronics. 30
  14. The direct-view display of claim 13 wherein the drive electronics comprise:
    - an address transistor, one for each modulator of the display, which is in electrical contact with the address electrode of the modulator, the address transistor having a gate and a drain, wherein the gate is electrically connected to a gate line, each row of modulators in the array having its own gate line, and the drain is electrically connected to a data line, each column of modulators in the array having its own data line; 35
    - a gate decoder which is in electrical contact with each address transistor in a row of modulators through the gate line; 40
    - shift registers for receiving a data signal, which shift registers are in electrical contact with a serial-to-parallel converter to parallel shift the data signal received by the shift registers; and 45
    - a driver amplifier electrically connected to the data line, each data line having its own driver amplifier, which receive the parallel shifted data signal, wherein once a data signal has been received by the shift registers and parallel shifted, the gate decoder activates one row of address transistors by sending a signal along the gate line that is electrically connected to the one row, biasing the gate of 50
- each address transistor in the one row, which allows the data signal from the data line to be delivered to the address electrode of the modulator which is located at that particular row and column address. 55

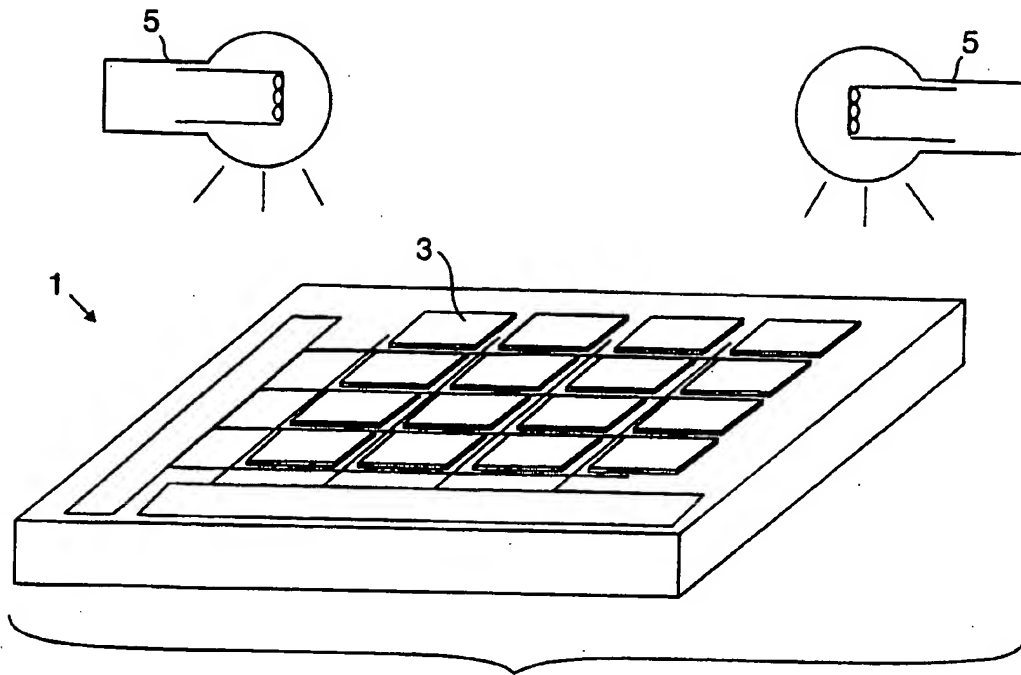


FIG. 1

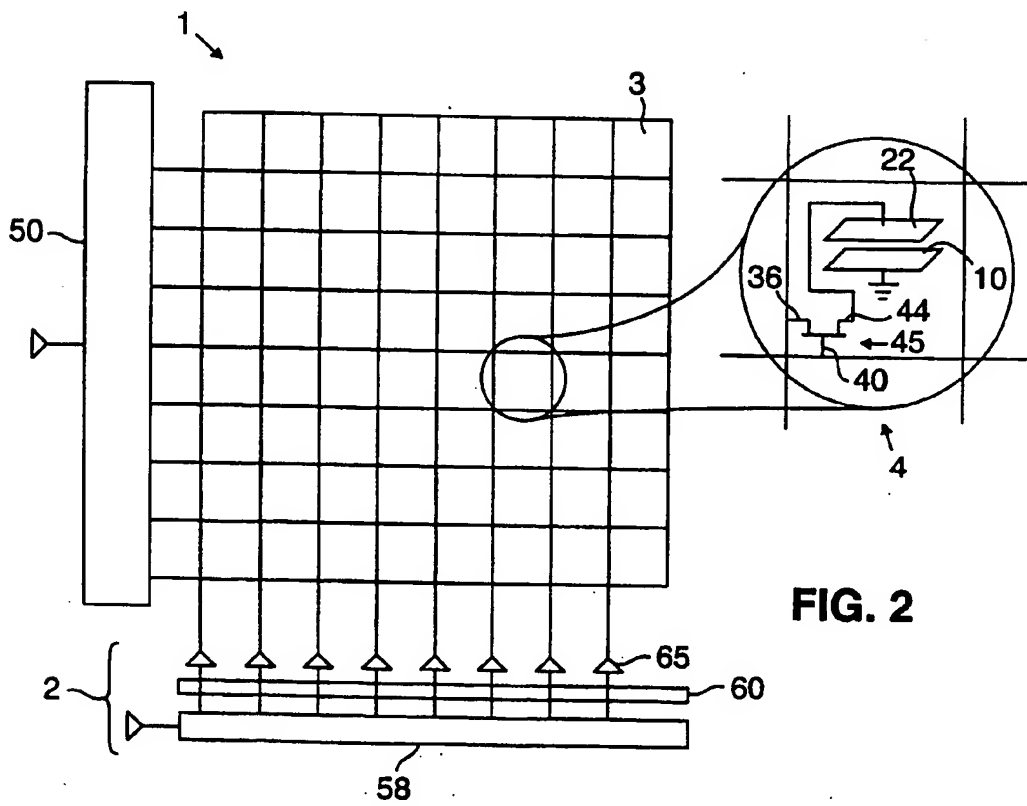
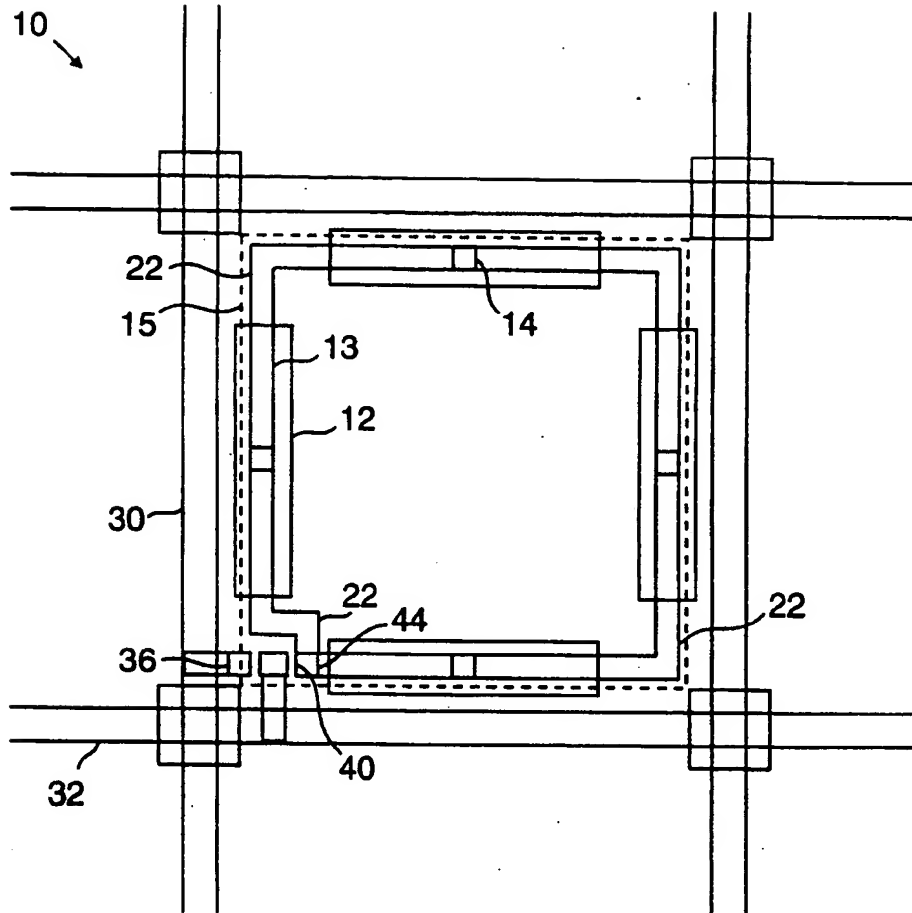
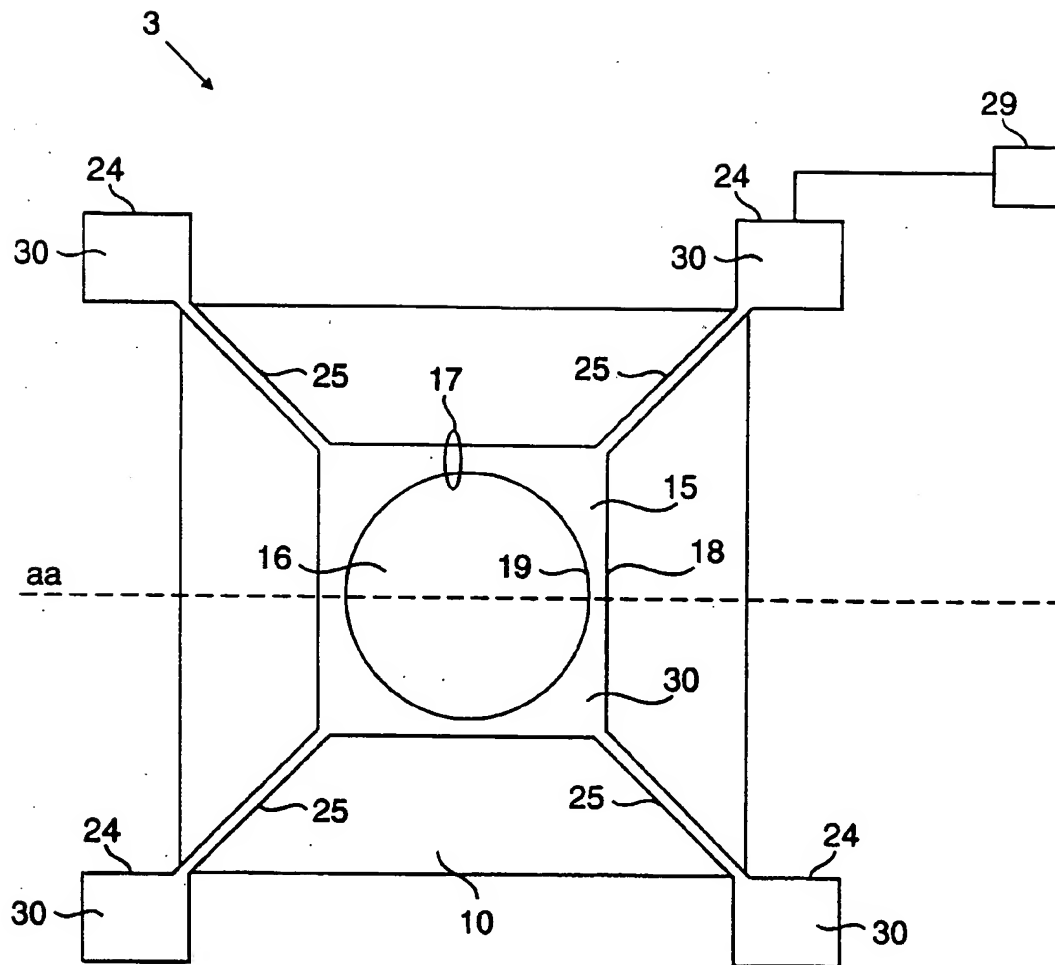


FIG. 2

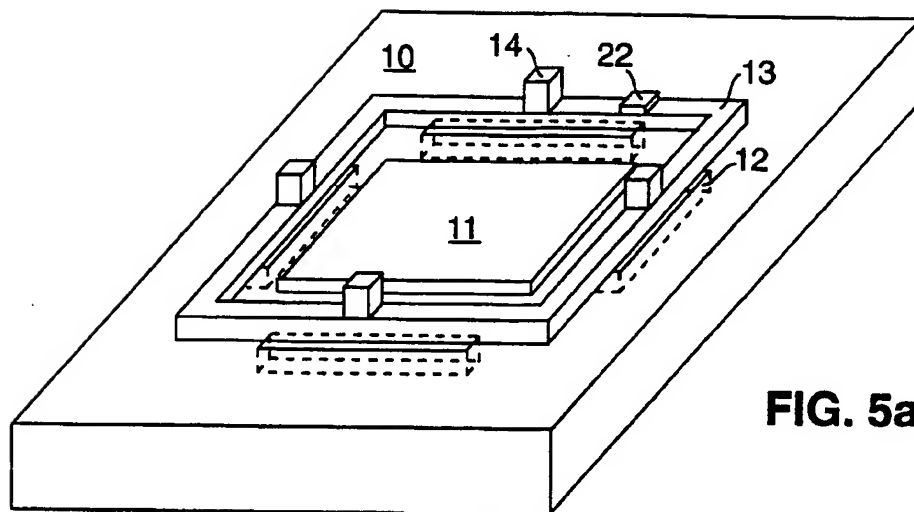




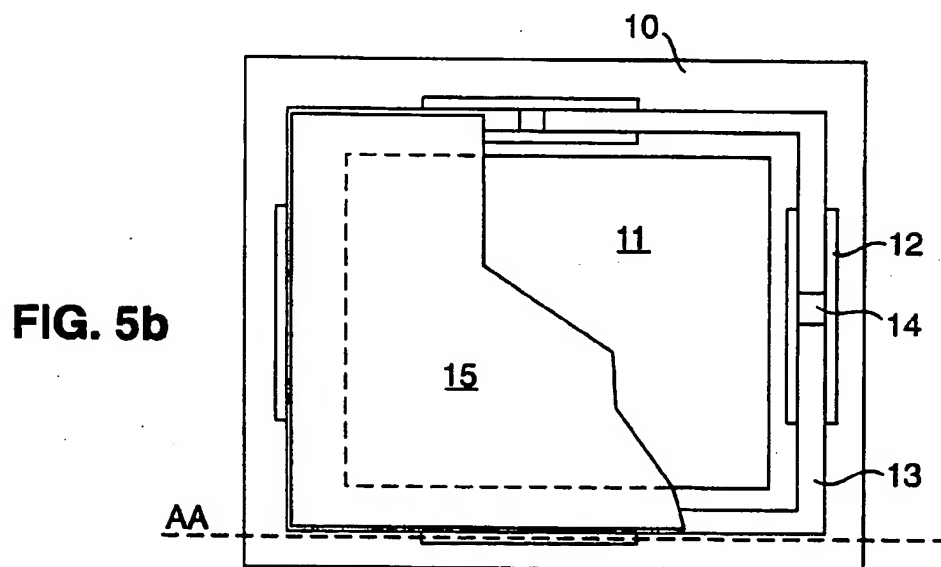
**FIG. 3**



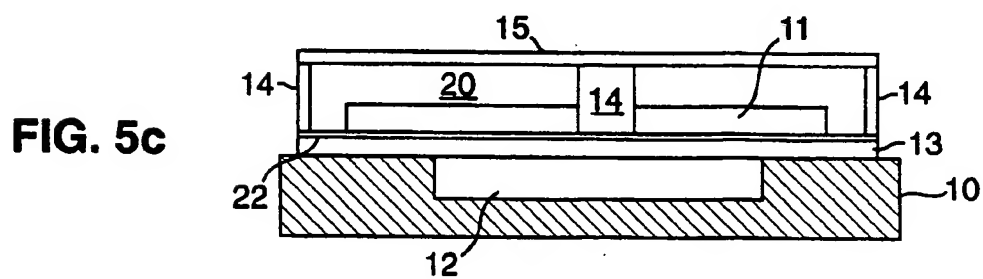
**FIG. 4**



**FIG. 5a**



**FIG. 5b**



**FIG. 5c**

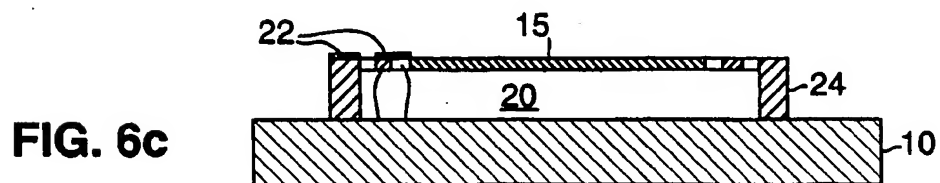
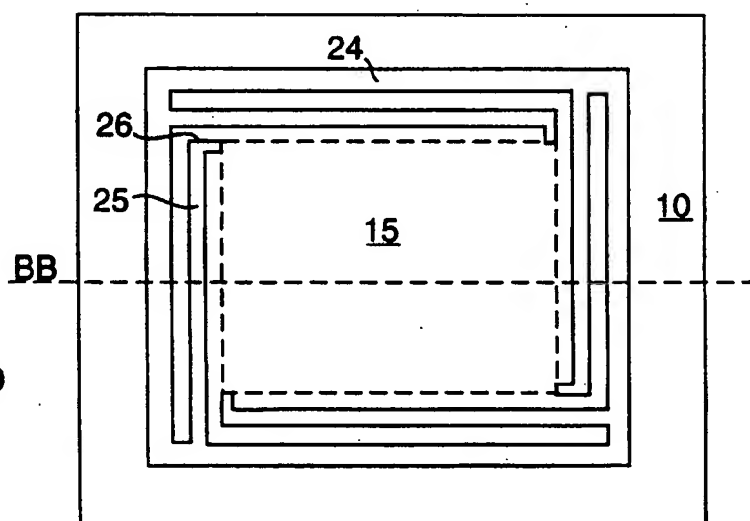
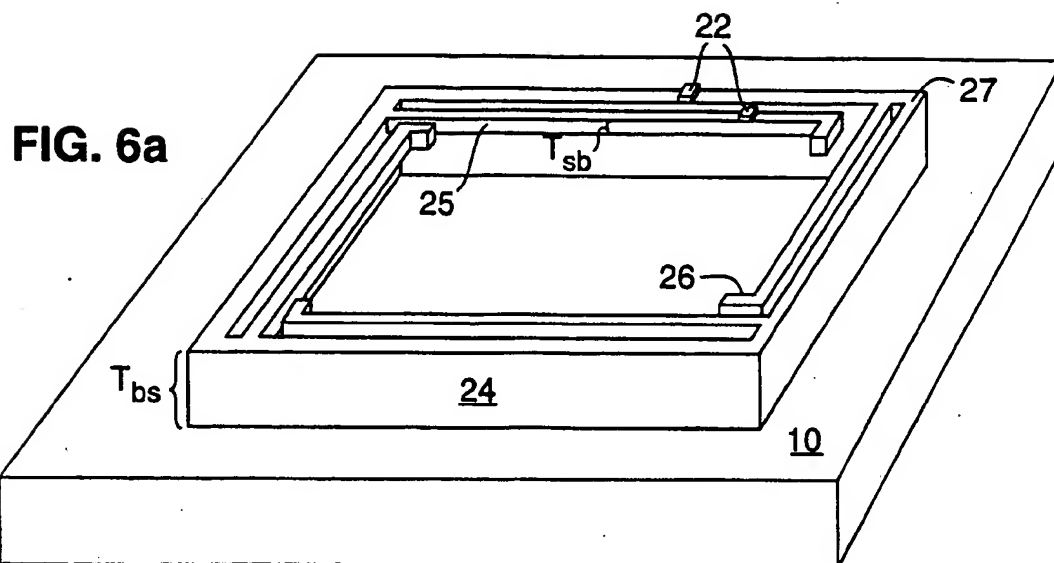


FIG. 7a

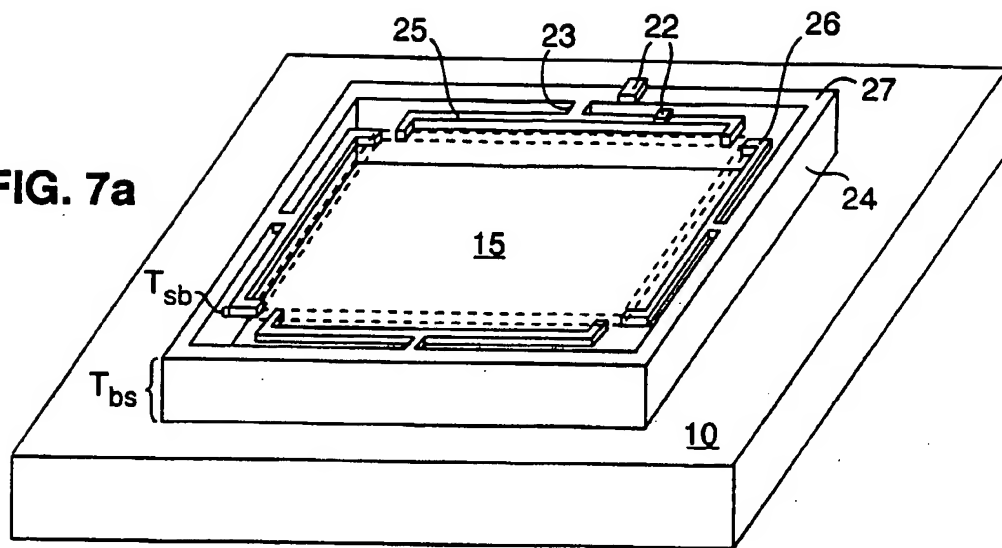


FIG. 7b

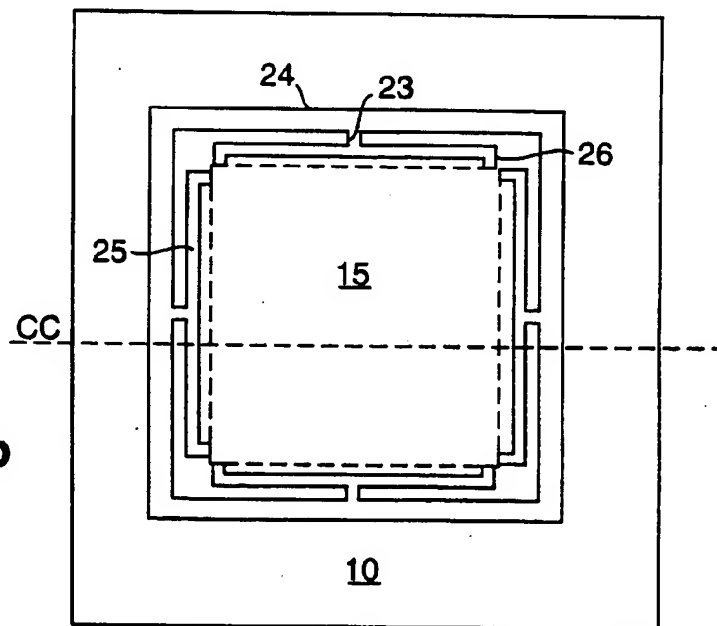
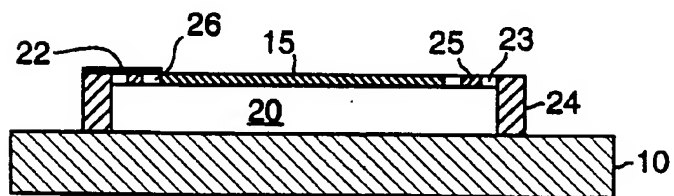


FIG. 7c



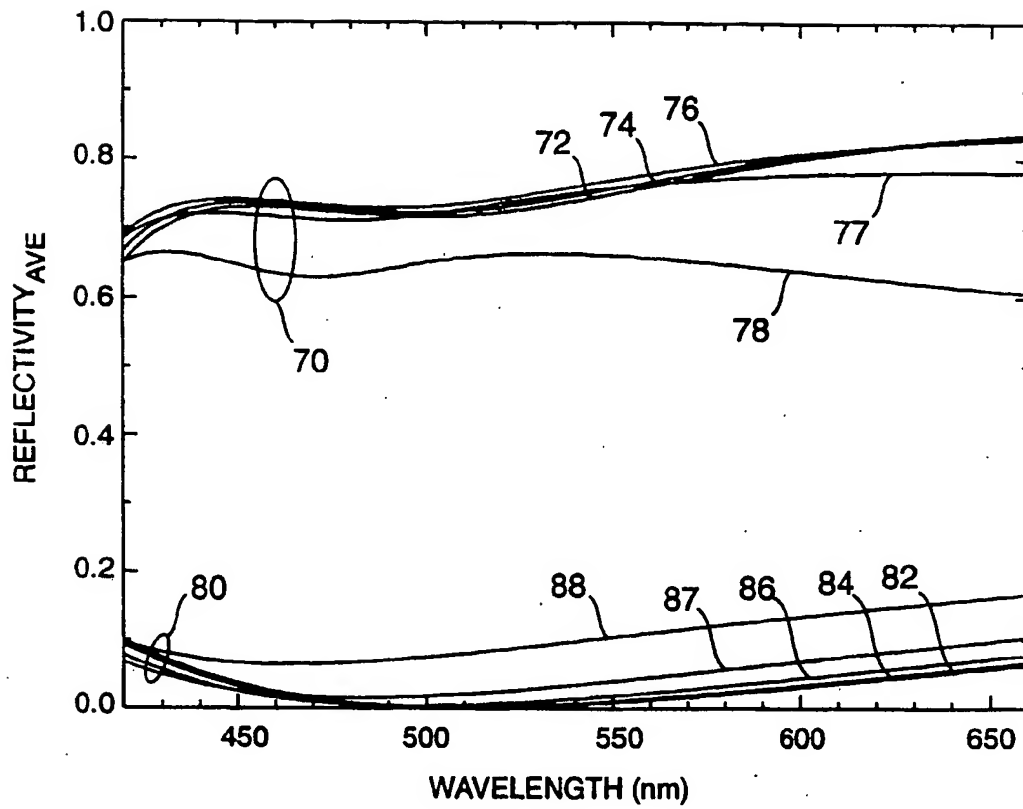


FIG. 8



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 95 30 5019

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	PROCEEDINGS OF THE WORKSHOP ON MICRO ELECTRO MECHANICAL SYSTEMS (ME, FORT LAUDERDALE, FEB. 7 - 10, 1993, no. WORKSHOP 6, 7 February 1993 INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, pages 230-235, XP 000366885 ARATANI K ET AL 'PROCESS AND DESIGN CONSIDERATIONS FOR SURFACE MICROMACHINED BEAMS FOR A TUNEABLE INTERFEROMETER ARRAY IN SILICON' * abstract; figures 3,4 * * page 230, right column, last paragraph - page 231, left column, paragraph 2 * * page 232, right column *	1,2,7-9, 11	G02B26/02 G09F9/37
X	EP-A-0 035 299 (PHILIPS NV) 9 September 1981 * abstract * * page 2, line 10 - line 15 * * page 3, line 34 - line 36 * * page 8, line 21 - page 9, line 4; claims 1,6,8; figure 2 *	1	
A	---	2-14	TECHNICAL FIELDS SEARCHED (Int.Cl.6) G02B G09F
X	WO-A-86 04182 (AMERICAN TELEPHONE & TELEGRAPH) 17 July 1986. * page 10, line 21 - page 11, line 13; figures 12,14 *	1	
A	---	2-14	
X	US-A-4 982 184 (KIRKWOOD JOHN M) 1 January 1991 * abstract; figures 2,5 * * column 1, line 27 - line 61; claim 8 *	1	
The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 13 November 1995	Examiner Hylla, W
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document</p> <p>T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons @: member of the same patent family, corresponding document</p>			

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